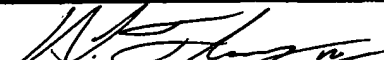




Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/658,897
				Filing Date	September 10, 2003
				First Named Inventor	Vijay S. Menon
				Art Unit	2189
				Examiner Name	Flournoy, Horace L.
Sheet	2	of	2	Attorney Docket Number	42P17027

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T?
HA		W. Pugh, The Java Memory Model is Fatally Flawed. Concurrency: Practice and Experience, vol. 12, no. 6, pp. 445-455, 2000.	
HA		M. Ahamad, et al. The Power of Processor Consistency. In Proceedings of the Fifth ACM Symposium on Parallel Algorithms and Architectures (SPAA), 1993.	
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Examiner Signature		Date Considered	5/30/2006
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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